

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Phase Change Memory Cell On Silicon-On Insulator Substrate

Application Number :

Confirmation Number:

First Named Applicant: Stephen Furkay

Attorney Docket Number: BUR920030147US1

Art Unit:

Examiner:

Search string: (6597009 or 6593176 or 6590807 or 6586761 or 6576921 or 6579318 or 5177717
or 20030128646 or 20030128568 or 20030123277 or 20030067013 or
20020144811),pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6597009	2003-07-22	Wicker			
	2	6593176	2003-07-15	Dennison			
	3	6590807	2003-07-08	Lowrey			
	4	6586761	2003-07-01	Lowrey			
	5	6576921	2003-06-10	Lowrey			
	6	6579318	2003-06-10	Lee, et al			
	7	5177717	1993-01-05	Sato, et al			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20030128646	2003-07-10	Nagle			
	2	20030128568	2003-07-10	Nagle			
	3	20030123277	2003-07-03	Lowrey et al			
	4	20030067013	2003-04-10	Ichihara, et al			
	5	20020144811	2002-10-10	Chou, et al			

Signature

Examiner Name	Date